

#2a
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Davis

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:
PIO

Serial No. **Not Yet Assigned**

Filing Date: **Herewith**

For: **ELECTRONIC MEMORY CIRCUIT
AND RELATED MANUFACTURING
METHOD**

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PRELIMINARY AMENDMENT

Assistant Commissioner for Patents
Washington, D.C. 20231

Sir:

Prior to the calculation of fees and examination of
the present application, please enter the amendments and
remarks set out below.

In the Drawings:

Submitted herewith is a request for a proposed
drawing modification to label FIGS. 1-5 prior art and to add a
reference numeral to FIG. 6 as indicated in red ink.

In the Claims:

Please cancel Claims 1-7.

Please add new Claims 8-25.

8. A method for manufacturing an EEPROM comprising
a matrix of memory cells arranged in pairs, the method
comprising:

forming active areas in a substrate;
forming doped regions in the substrate;